

ABSTRACT OF THE DISCLOSURE

An integrated circuit according to the present invention includes first, second and third plane-like metal layers. A first transistor has a first control terminal and first and second terminals. The second terminal communicates with the first plane-like metal layer. The first terminal communicates with the second plane-like metal layer. A second transistor has a second control terminal and third and fourth terminals. The third terminal communicates with the first plane-like metal layer. The fourth terminal communicates with the third plane-like metal layer. A fourth plane-like metal layer includes first, second and third contact portions that are electrically isolated from each other and that are connected to the second plane-like metal layer, the first plane-like metal layer and the third plane-like metal layer, respectively.